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**SIGMA DESIGNS TO SHOWCASE DOCSIS 3.0 MULTI CHANNEL
AND TRU2WAY™ SET-TOP BOX AT NCTA CABLE SHOW 2008**

*Sigma Designs extends its Telco IPTV set-top box expertise to Cable IP set-top box and
HDTV manufacturers*

MILPITAS, Calif. – May 15, 2008 – Sigma Designs (Nasdaq: SIGM), a leader in digital media processing system-on-a-chip (SoC) solutions for consumer electronics, leveraging Texas Instruments field proven cable TV technology and VividLogic’s comprehensive tru2way™ software for set-top boxes (STBs) and cable TV ready HDTVs, announces demonstrations of its DOCSIS 3.0 multi channel and tru2way™ set-top box at the NCTA Cable Show 2008, May 18-20 in New Orleans. The demonstrations will take place at Sigma Designs’ stand in the CableNET zone and in VividLogic booth #3929.

“The cable TV industry is upgrading its infrastructure to enable higher speed downstream and upstream communication. The switched digital system in a Cable TV plant provides an unlimited number of high definition channels and video-on-demand functionality to the consumer,” said Ken Lowe, Sigma’s VP of strategic marketing. “Sigma Designs will leverage its expertise in the Telco IPTV set-top box market to support IP-Cable STBs and HDTV manufacturers. We are proud to offer consumers more competitive choices in their home entertainment experience.”

Sigma's Cable-IP Set-Top Box Demonstrations

Sigma Designs will be showcasing the DOCSIS 3.0 (1.X and 2.0) based STB powered by Sigma's SMP8634 Secure Media Processors, capable of:

- Fast upstream and downstream communication

- High speed cable modem for Internet connectivity

- Web browsing

- Tru2way™ middleware for downloadable EPG and interactive Java applications

- Decoding of multiple video streams for simultaneous viewing and recording

- Multi standard decoding (MPEG-2, H.264, VC-1)

- Unlimited Cable TV channel lineup on "IPTV like" switched digital video & VoD cable plant

- Support IPv6 128 bits addresses

More about Sigma's DOCSIS 3.0 (1.X and 2.0)-based Set-Top Box

Texas Instruments developed the Puma-5 DOCSIS 3.0 based chipset used in the Sigma Designs set-top box. VividLogic developed the Open Cable Host (OCHD 2.0) based tru2way™ middleware that includes OCAP, MCARD, DSG, IEEE-1394, DVR and other OCHD 2.0 components, being ported to run on the Sigma Designs' SMP8634 media processor.

Optimized for triple play and next generation IP services, TI's Puma 5 family has been developed to support DOCSIS 3.0 enhanced performance and features. New DOCSIS 3.0 features such as channel bonding enable ultra high downstream bandwidth rates of at least 160 Mbps in the residential data and voice services configuration. In addition, the solution offers 320 Mbps in video and business services configuration.

"TI is excited to deliver the Puma 5 to this video segment," said Irvind Ghai, Director Connectivity and Cable Business at TI. "As the market leader in DOCSIS 3.0 based technology, Texas Instruments is happy to bring the differentiating capabilities of this technology to next generation Cable set-top boxes."

“We are delighted to work with Sigma Designs and TI in porting our Open Cable Host (OCHD 2.0) based tru2way™ software to Sigma Designs’ set-top box reference platform,” said Shiva Patibanda, CEO VividLogic Inc. “Our comprehensive tru2way™ software solution will accelerate time-to-market for Sigma Designs and TI customers.”

Industry estimates show that Sigma's SMP8634 chip is in approximately 80% of the IPTV set-top boxes worldwide, more than any other semiconductor offered to the industry, making Sigma a leader in global IPTV services.

About Sigma's SMP8634 Media Processor

Sigma’s SMP8634 media processor integrates a complete complement of next-generation capabilities for a single-chip system-on-chip (SoC) solution with powerful multimedia processing, robust content security system, and a full complement of peripherals. Its advanced decoder engines support video decoding of H.264 (MPEG-4 part 10), Windows Media® Video 9, VC-1, MPEG-2 and MPEG-4 (part 2) with multiple streams, up to the equivalent of two high-definition video streams. High-performance graphics acceleration, multi-standard audio decoding, advanced display processing capabilities, and HDMI/HDCP output round out its multimedia core. Powerful content security is ensured through a dedicated secure processor, flash memory, and a range of digital rights management (DRM) engines for high-speed payload decryption. The SMP8634’s 300-MHz host CPU, 3.2 GB/second unified memory controller, Ethernet 10/100 controller, dual USB 2.0 controller, and IDE controller provide for a single-chip solution for most consumer products.

About Sigma Designs, Inc.

Sigma Designs is a leading fabless provider of highly integrated system-on-chip, or SoC, solutions that are used to deliver multimedia entertainment throughout the home. Sigma’s SoC solutions combine its semiconductors and software and are a critical component of multiple high-growth, consumer applications that process digital video and audio content, including internet protocol TV, or IPTV, high definition DVD players, high definition TVs, or HDTVs, and portable media players. Headquartered in Milpitas, Calif., Sigma Designs also has sales representatives in the United States, Belgium, China, Japan and Taiwan and sells its products through a third-party distributor in Korea. For more information, please visit Sigma Designs’ web site at www.sigmadesigns.com.